AMENDMENTS

In the Claims:

1. (Currently Amended) A system, comprising:

a synchronizer signal generator device, ~~which is~~ to be connected to an electronic system, ~~and which~~ the synchronizer signal generator device to emit ~~emits~~ a synchronizer signal of a particular frequency, ~~which is~~ the synchronizer signal to be transferred to at least one receiving device of the electronic system; ~~, wherein the~~

at least one additional device, ~~of~~ for which an impedance is chosen such that a resonance-oscillatory circuit is created [[,]] for the synchronizer signal generator device [[,]] ~~of which the~~ having a resonance frequency which essentially coincides with the frequency of the synchronizer signal; and

a clock generator device to generate a clock signal having a frequency, wherein

the clock generator device is controlled by the synchronizer signal, and

the frequency of the synchronizer signal is greater than the frequency of the clock signal.

2. (Currently Amended) The ~~synchronizer signal generator device~~ system according to Claim 1, wherein the synchronizer signal transferred to ~~received by~~ the at least one receiving device is essentially sinusoid.

3. (Currently Amended) The ~~synchronizer signal generator device~~ system according to Claim 1, further comprising a driver device ~~for generating~~ to generate the synchronizer signal.

4. (Currently Amended) The ~~synchronizer signal generator device~~ system according to Claim 3, wherein the driver device generates an essentially rectangular signal.

5. (Currently Amended) The ~~synchronizer signal generator device~~ system according to Claim 4, wherein the rectangular signal generated by the driver device is to be filtered such that the signal emitted by the synchronizer signal generator device is essentially sinusoid.

6. (Currently Amended) The ~~synchronizer signal generator device~~ system according to Claim 1, further comprising at least one impedance device, which has an inductive component.

7. (Currently Amended) The ~~synchronizer signal generator device~~ system according to claim 6, wherein the at least one impedance device has a capacitive component.

8. (Currently Amended) The ~~synchronizer signal generator device~~ system according to Claim 7, wherein an inductivity and/or capacitance adjustment of the inductive and/or capacitive component is set during manufacture.

9. (Currently Amended) A ~~synchronizer signal generator device~~ system according to Claim 8, wherein the inductivity and/or the capacitance of the inductive and/or capacitive component is variably adjustable after manufacture.

10. (Currently Amended) The ~~synchronizer signal generator device~~ system according to Claim 9, wherein the capacitive component is a capacitive diode.

11. (Currently Amended) The ~~synchronizer signal generator device~~ system according to Claim 1, wherein the at least one receiving device to which the synchronizer signal is to be transferred, is a semi-conductor component.

12. (Currently Amended) The ~~synchronizer signal generator device~~ system according to Claim 1, wherein the synchronizer signal ~~of the device~~ is to be used for chronological co-ordination of relaying and/or processing and/or transfer of data.

13. (Currently Amended) The ~~synchronizer signal generator device~~ system according to Claim 1, wherein the synchronizer signal generator device generates a further signal under control of the synchronizer signal, which is to be used for chronological co-ordination of relaying and/or processing and/or transfer of data.

14. (Currently Amended) The ~~synchronizer signal generator device~~ system according to Claim 13, wherein the further signal has a lower frequency than the synchronizer signal.

15. (Currently Amended) The ~~synchronizer signal generator device~~ system according to Claim 14, wherein a PLL or DLL circuit is used to generate the further signal.

16. (Currently Amended) A process for generating a synchronizer, comprising:

emitting a synchronizer signal [[by]] from a synchronizer signal generator device to at least one receiving device of an electronic system; ~~and~~

providing [[the]] at least one additional device, ~~in the synchronizer signal generator device and/or the electronic system, of~~ for which an impedance has been selected such that~~, for the synchronizer signal generator device~~, a resonance-oscillatory circuit is created for the synchronizer signal generator device, ~~of which~~ the resonance-oscillatory circuit having a resonance frequency which essentially coincides with a frequency of the synchronizer signal; and

generating a clock signal at a clock generator device, wherein

the clock generator device is controlled by the synchronizer signal, and

the frequency of the synchronizer signal is greater than a frequency of the clock signal.